IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Atty. Docket No.: 001701.00059

Yoshiaki FUKUZUMI et al.

Serial No.: 09/783,023 Group Art Unit: 2823

Filed: February 15, 2001 Examiner: G. Fourson, III

For: SEMICONDUCTOR DEVICE USING Confirmation No.: 6061

FUSE/ANTI-FUSE SYSTEM AND METHOD
OF MANUFACTURING THE SAME

Attn: G. Fourson, III Fax No.: 703-308-7382

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PTO/SB/31 (05-03)

10/14/03 Date

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NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*

forms are submitted.

Registration number if acting under 37 CFR 1.34(a).__

This collection of information is required by 37 CFR 1.191. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiship is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including aphering, one parting, and submitted to provide the provided application form to the USPTO. Time will vary depending upon the individual cases, Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, V.A. 22131-1450, DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patenta, P.O. Box 1459, Alexandria, V.A. 223131-1450.